Abstracts

A Low-Noise GaAs Monolithic Broad-Band Amplifier Using a Drain Current Saving Technique (Short Papers)

K. Osafune, N. Kato, T. Sugeta and Y. Yamao. "A Low-Noise GaAs Monolithic Broad-Band Amplifier Using a Drain Current Saving Technique (Short Papers)." 1985 Transactions on Microwave Theory and Techniques 33.6 (Jun. 1985 [T-MTT]): 543-545.

A low-noise and low-power GaAs monolithic broad-band amplifier is proposed and has been developed, which has a new cascade connection with a large gate-width input FET and the other circuits in such a way that the output stage current flows through the input FET. The fabricated amplifier operates on +5-V single supply voltage, and provides a 3.3-dB noise figure, less than 180-mW power dissipation, and a 10-MHz--2.0-GHz bandwidth with 16-dB gain.

❑ Return to main document.